### TAIWAN **IICONDUCTOR** COMPLIANCE TO-220 **ITO-220** Pin Definition: 1. Gate 2. Drain 3. Source



TO-252 (DPAK)

#### Features

- Low R<sub>DS(ON)</sub> 1.2Ω (Typ.)
- Low gate charge typical @ 12.4nC (Typ.)
- Low Crss typical @ 18pF (Typ.)

#### **Ordering Information**

Part No.	Package	Packing			
TSM5NB50CH C5G	TO-251	75pcs / Tube			
TSM5NB50CP ROG	TO-252	2.5Kpcs / 13" Reel			
TSM5NB50CZ C0	TO-220	50pcs / Tube			
TSM5NB50CI C0	ITO-220	50pcs / Tube			
Note: "G" denotes for Hologon Free					

**Note:** "G" denotes for Halogen Free

#### Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

#### Limit Parameter Unit Symbol IPAK/DPAK ITO-220 **TO-220 Drain-Source Voltage** $V_{DS}$ 500 V V Gate-Source Voltage $V_{GS}$ ±30 $Tc = 25^{\circ}C$ 4.4 А **Continuous Drain Current** $I_D$ $Tc = 100^{\circ}C$ 2.4 А Pulsed Drain Current \* 17.6 А I<sub>DM</sub> Single Pulse Avalanche Energy (Note 2) 100 $\mathsf{E}_{\mathsf{AS}}$ mJ Total Power Dissipation @ $T_c = 25^{\circ}C$ 54 33 70 W $\mathsf{P}_{\mathsf{TOT}}$ 150 °C **Operating Junction Temperature** $T_{\rm J}$ °C Storage Temperature Range T<sub>STG</sub> -55 to +150

Note: Limited by maximum junction temperature

#### **Thermal Performance**

Devenetor	Symbol		Unit		
Parameter		IPAK/DPAK	ITO-220	TO-220	Unit
Thermal Resistance - Junction to Case	Rθ <sub>JC</sub>	2.3	3.8	1.78	°C/W
Thermal Resistance - Junction to Ambient	RƏ <sub>JA</sub>	83	62.5	62.5	°C/W

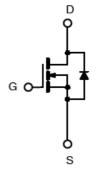
#### PRODUCT SUMMARY

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
500	1.5 @ V <sub>GS</sub> =10\	/ 4.4

#### **General Description**

The TSM5NB50 N-Channel Power MOSFET is produced by new advance planar process. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

**Block Diagram** 



N-Channel MOSFET

TSM5NB50

### 500V N-Channel Power MOSFET



#### Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = 250uA$	BV <sub>DSS</sub>	500			V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 2.2A$	R <sub>DS(ON)</sub>		1.2	1.5	Ω
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 uA$	V <sub>GS(TH)</sub>	2.5	3.5	4.5	V
Zero Gate Voltage Drain Current	$V_{DS} = 500V, V_{GS} = 0V$	I <sub>DSS</sub>			1	uA
Gate Body Leakage	$V_{GS} = \pm 30V, V_{DS} = 0V$	I <sub>GSS</sub>			±100	nA
Forward Transfer Conductance	$V_{DS} = 10V, I_{D} = 2.2A$	<b>g</b> <sub>fs</sub>		3.5		S
Dynamic						
Total Gate Charge	$V_{DS} = 300V, I_D = 4.4A,$ $V_{GS} = 10V$	Qg		12.4		nC
Gate-Source Charge		$Q_gs$		2.9		
Gate-Drain Charge	(Note 4,5)	$Q_gd$		5.5		
Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz	C <sub>iss</sub>		552		pF
Output Capacitance		C <sub>oss</sub>		83		
Reverse Transfer Capacitance		C <sub>rss</sub>		18		
Switching						
Turn-On Delay Time		t <sub>d(on)</sub>		12		nS
Turn-On Rise Time	$V_{GS} = 10V, I_D = 4.4A,$	t <sub>r</sub>		22		
Turn-Off Delay Time	V <sub>DD</sub> = 300V, R <sub>G</sub> =25Ω (Note 4,5)	t <sub>d(off)</sub>		33		
Turn-Off Fall Time		t <sub>f</sub>		21		
Source-Drain Diode Ratings and C	naracteristic					
Source Current	Integral reverse diode in	I <sub>S</sub>			4.4	А
Source Current (Pulse)	the MOSFET	I <sub>SM</sub>			16	А
Diode Forward Voltage	$I_{S} = 4.4A, V_{GS} = 0V$	$V_{SD}$		0.9	1.5	V

Note 1: Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

**Note 2:**  $V_{DD} = 50V$ ,  $I_{AS}=2.2A$ , L=40mH,  $R_G = 25\Omega$ , Starting  $T_J=25^{\circ}C$ 

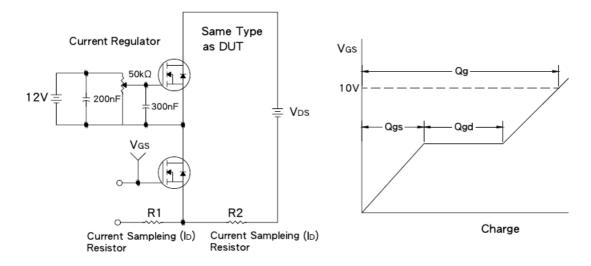
**Note 3:** I<sub>SD</sub>≤4A, di/dt≤200A/uS, V<sub>DD</sub>≤BV<sub>DSS</sub>, Starting T<sub>J</sub>=25°C

Note 4: Pulse test: pulse width ≤300uS, duty cycle ≤2%

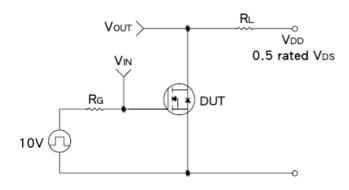
Note 5: Essentially Independent of Operating Temperature

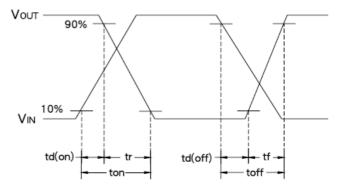


#### Gate Charge Test Circuit & Waveform

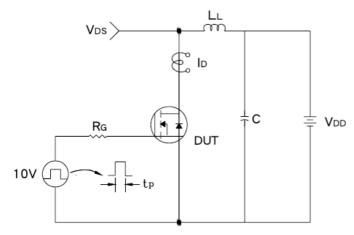


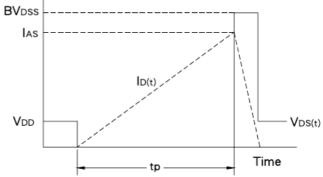
#### **Resistive Switching Test Circuit & Waveform**





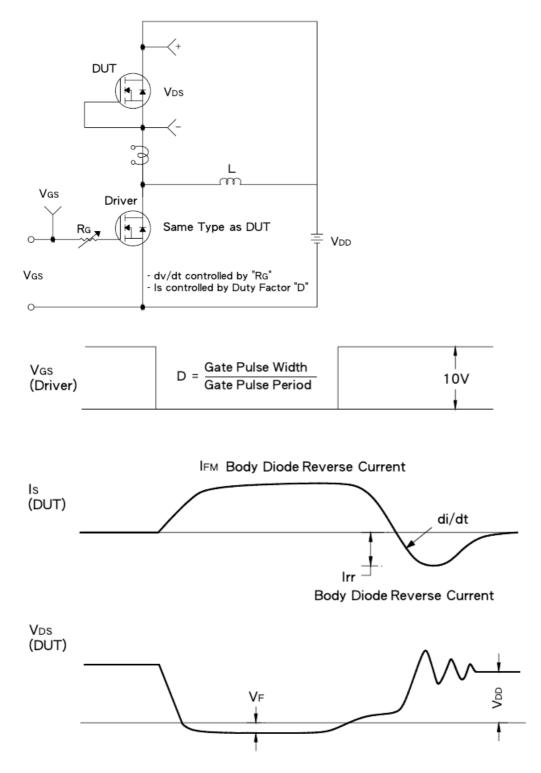
#### **E**AS Test Circuit & Waveform





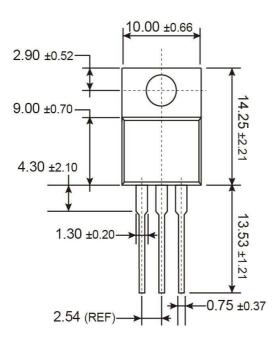


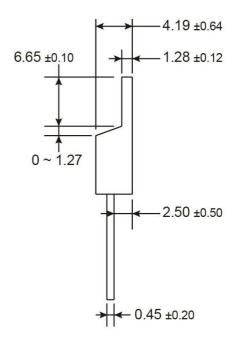
#### **Diode Reverse Recovery Time Test Circuit & Waveform**





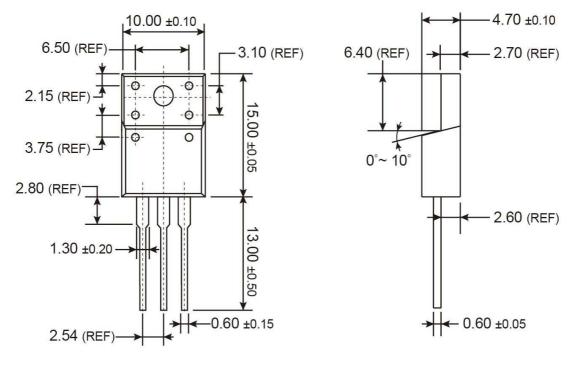
### **TO-220 Mechanical Drawing**







### **ITO-220 Mechanical Drawing**



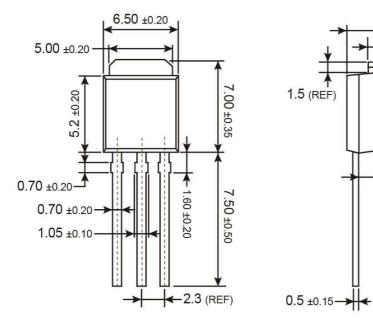


2.30 ±0.20

0.5 (REF)

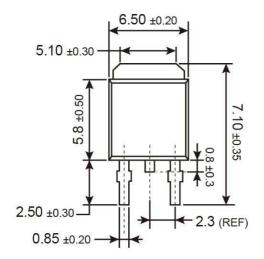
-1.00 ±0.15

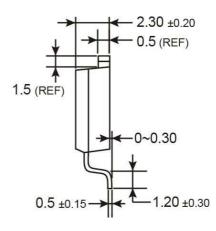
### **TO-251 Mechanical Drawing**





### **TO-252 Mechanical Drawing**







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